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03/10/2005 CNGUYEN1 00000044 10797253

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Fran Petrillo (Depositor's name)  
Fran Petrillo (Signature)  
March 9, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10797,253	03/10/2004	Hiroshige Hirano	MTS-2101US3	5368

TITLE OF INVENTION: VOLTAGE DETECTION CIRCUIT, POWER-ON/OFF RESET CIRCUIT, AND SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1,718.00	03/10/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRA. ANH QUAN	2816	327-143000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
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Matsushita Electric Industrial Co., Ltd., Osaka, JAPAN

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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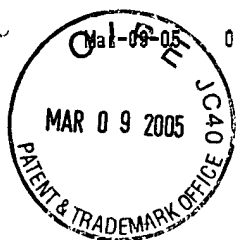
Date March 9, 2005

Typed or printed name Daniel N. Calder

Registration No. 27,424

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DATE: March 9, 2005

TIME: 3:15 pm

TO:	USPTO	FAX NO.:	(703) 746-4000
FROM:	Daniel N. Calder	ADMIN. ASST.:	Fran Petrillo
APPLN. NO.:	10/797,253	ATTY. DOCKET NO.:	MTS-2101US3
TITLE OF APPLN.: VOLTAGE DETECTION CIRCUIT, POWER-ON/OFF RESET CIRCUIT, AND SEMICONDUCTOR DEVICE			
FILING DATE:	March 10, 2004	ART UNIT:	2816
FIRST INVENTOR:	Hiroshige HIRANO, et al.	CONF. NO.:	5368
TITLE OF DOCUMENT (and List of Attachments):		Issue Fee Transmittal, Transmittal Form, and Credit Card Payment Form	
CUSTOMER NO. 23122			

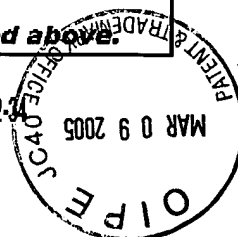
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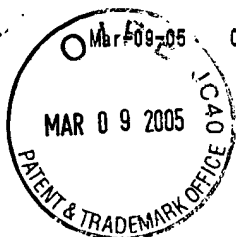
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	10/797,253
	Filing Date	March 10, 2004
	First Named Inventor	Hiroshige HIRANO, et al.
	Art Unit	2816
	Examiner Name	Anh Quan Tra
	Attorney Docket No.	MTS-2101US3
Total Number of Pages in This Submission 4		

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Signature			
Printed Name	Daniel N. Calder		
Date	March 9, 2005	Reg. No.	27,424

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Typed or Printed Name	Fran Petrillo	Date	March 9, 2005

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